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VLSI Digital Signal Processing Systems: Design and Implementation, Keshab K. Parhi, ISBN: 978-0471241867, Wiley, 1999. Suggested references The Design Warrior's Guide to FPGAs, Devices, Tools and Flows, Clive "Max" Maxfield, ISBN: 0750676043 Digital Signal Processing with Field Programmable Gate Arrays, Uwe Meyer-Baese, 3rd

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## **NOC | VLSI Signal Processing**

1. ``VLSI Digital Signal Processing Systems'', Keshab K. Parhi, Wiley Eastern
2. ``Digital Signal Processing for Multimedia Systems'', Keshab K. Parhi and Takao Nishitani, Marcel Dekker.
3. ``Pipelined Lattice and Wave Digital Recursive Filters'', J. G. Chung and Keshab K. Parhi, Kluwer.

## **VLSI Signal Processing - Course**

Journal of VLSI Signal Processing, 9, 121-143 (1995), Kluwer Academic Publishers, Boston. Keshab K. Parhi and David G. Messerschmitt, Pipeline Interleaving and Parallelism in Recursive Digital Filters - Part I: Pipelining Using Scattered Look-Ahead and Decomposition.

### **VLSI Programming [2IN35]**

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